

FIG. 1

PRIOR ART

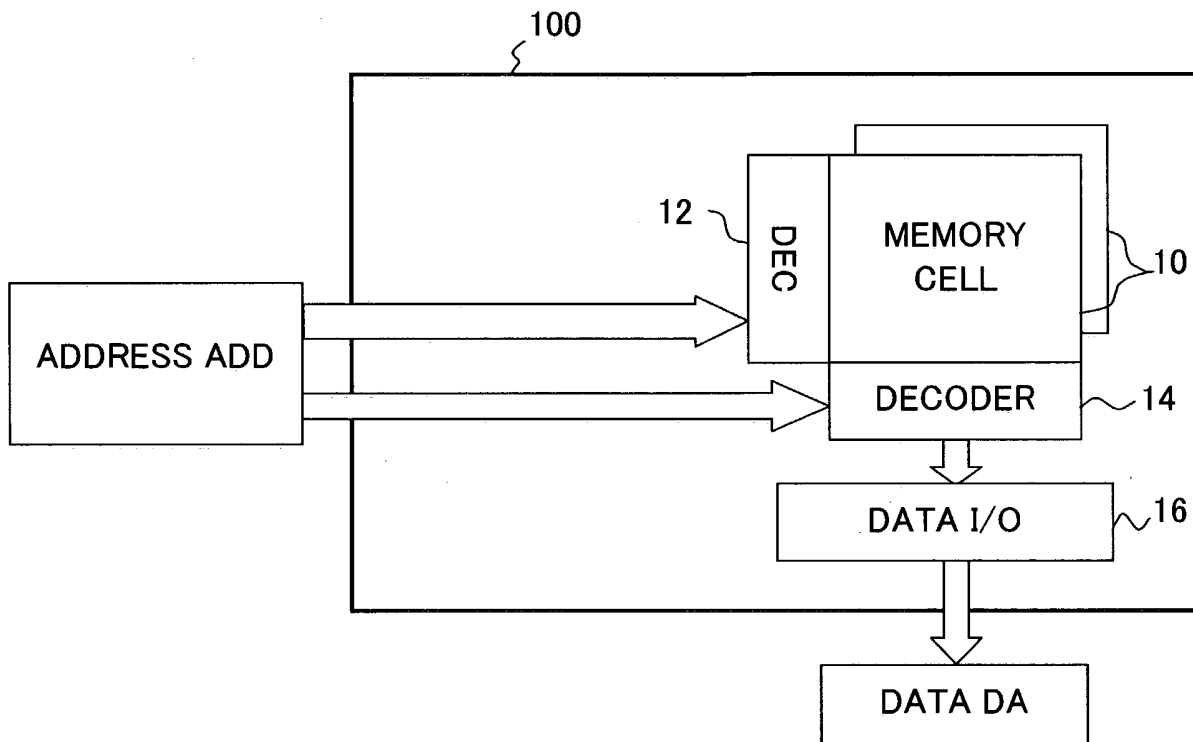


FIG. 2

PRIOR ART

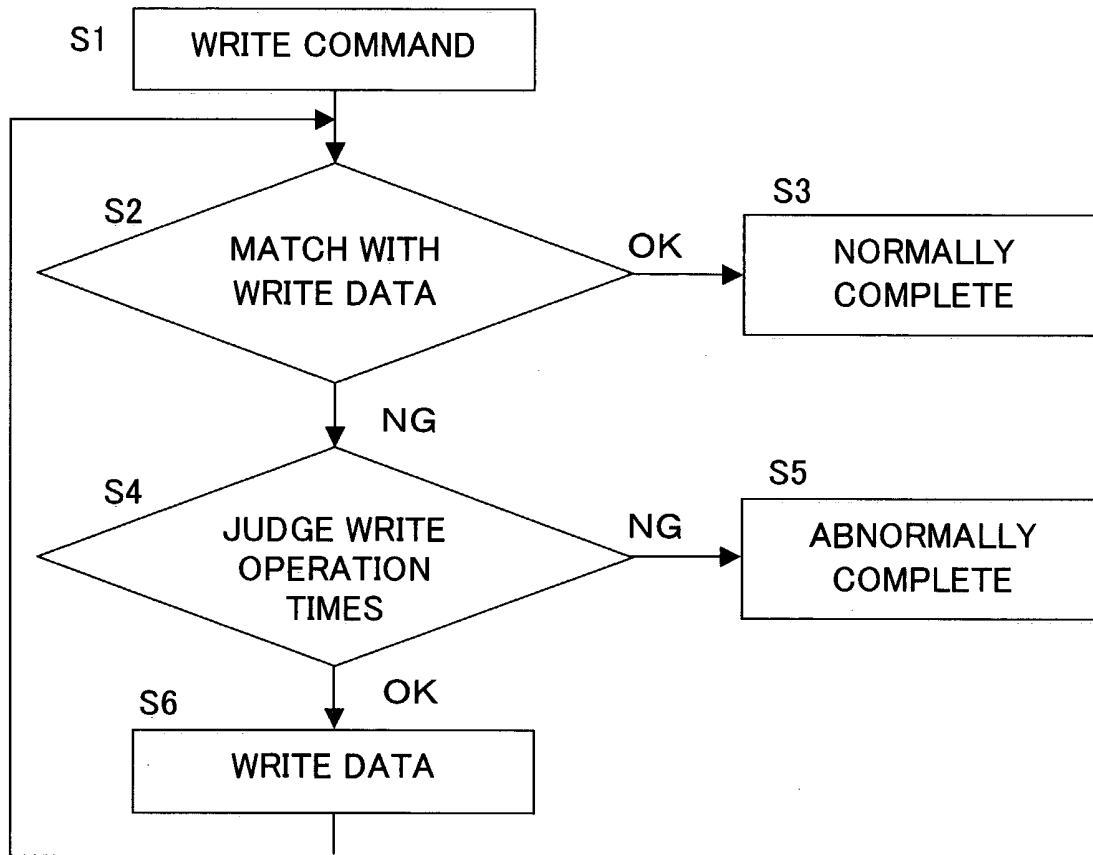


FIG. 3

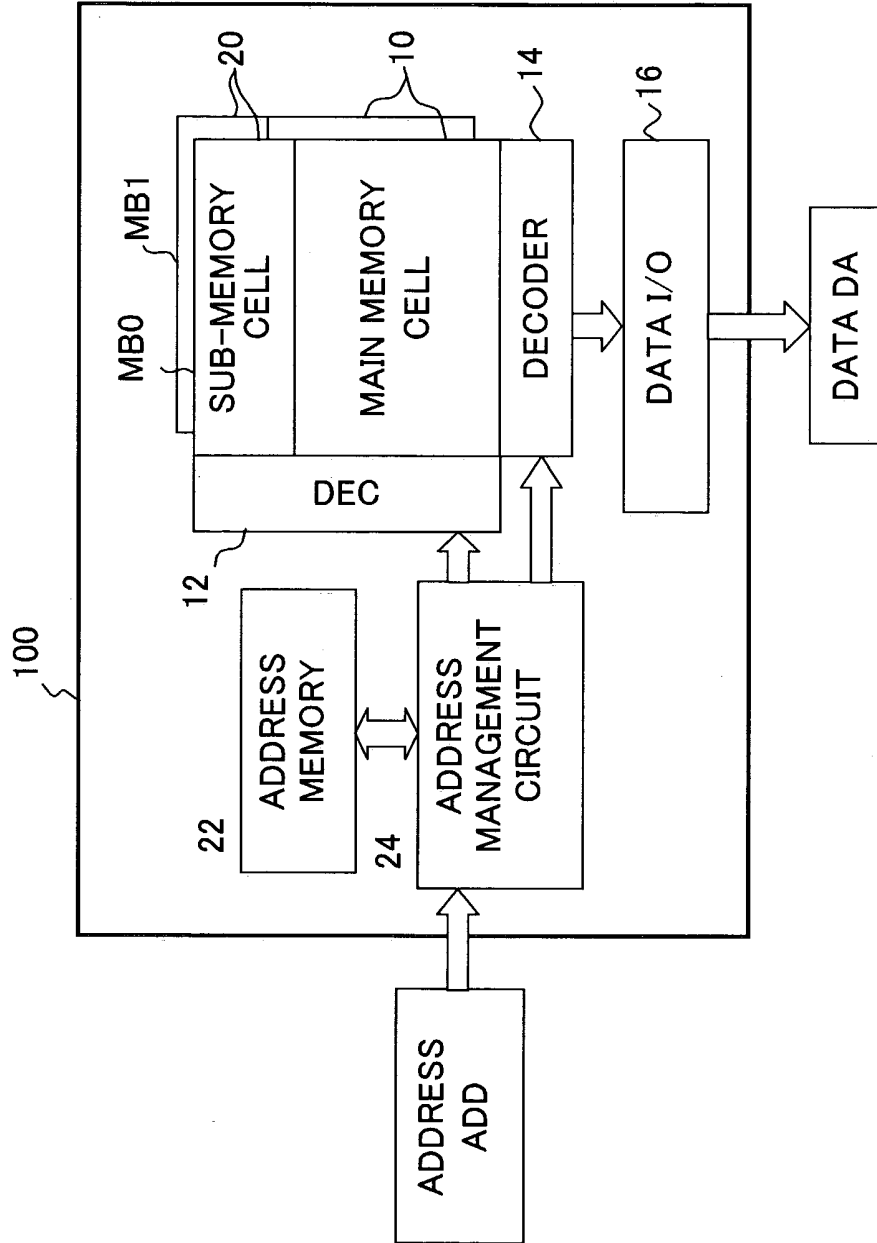


FIG. 4

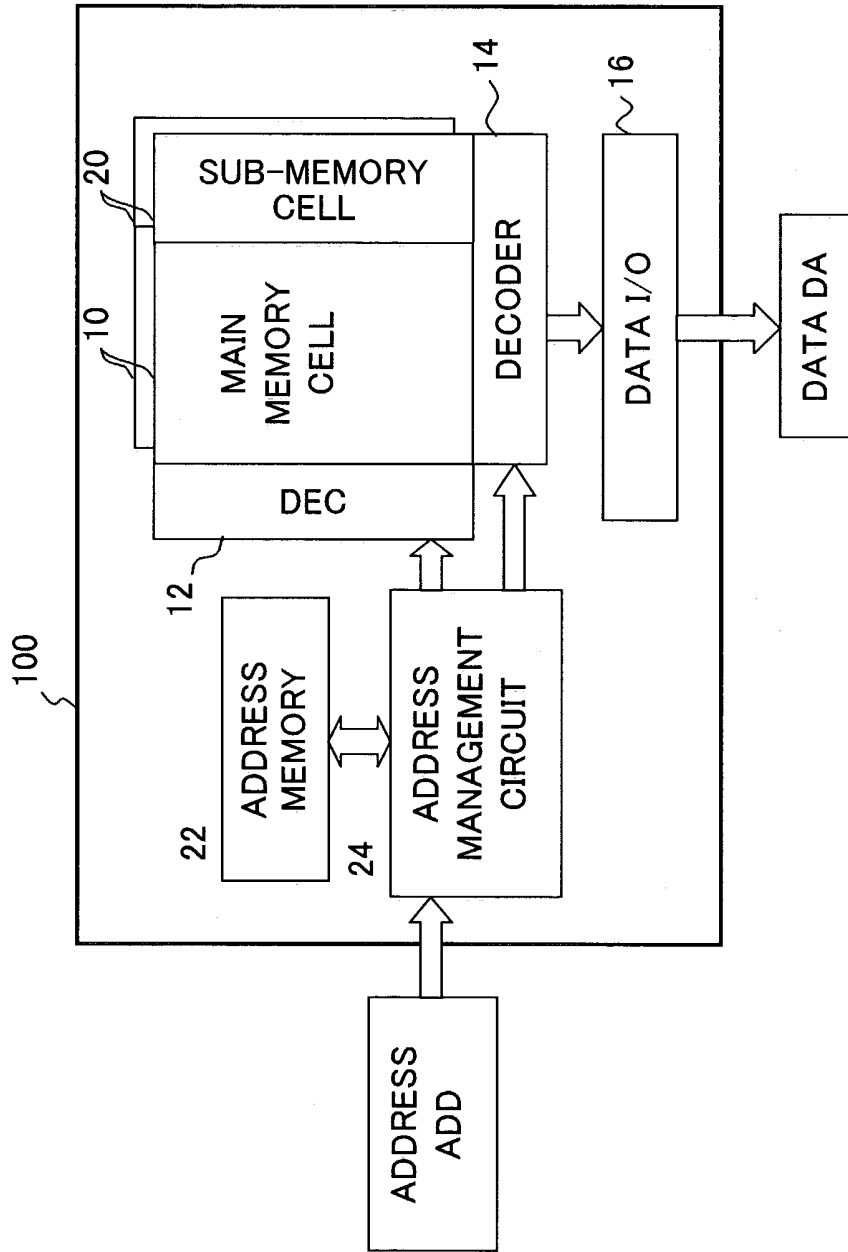


FIG. 5

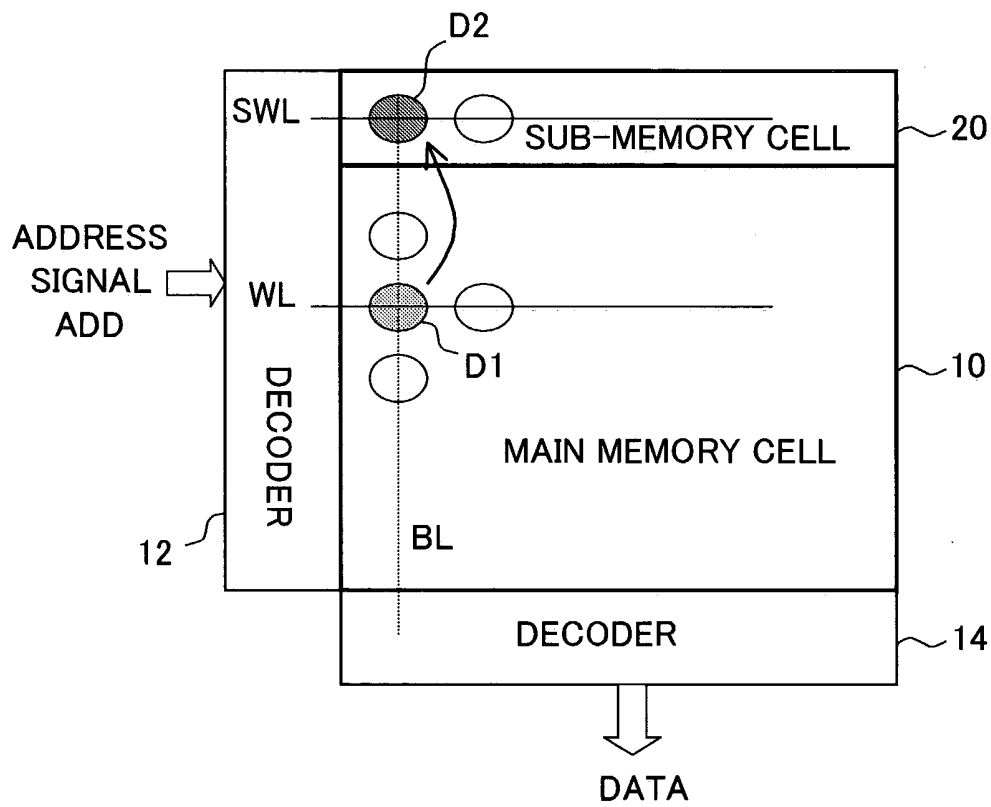


FIG. 6

22

MAIN MEMORY ADDRESS ADD	SUB-MEMORY ADDRESS SADD
ADD1	000
ADD2	001
	010
	011
	.
	.
	.
	111

FIG. 7

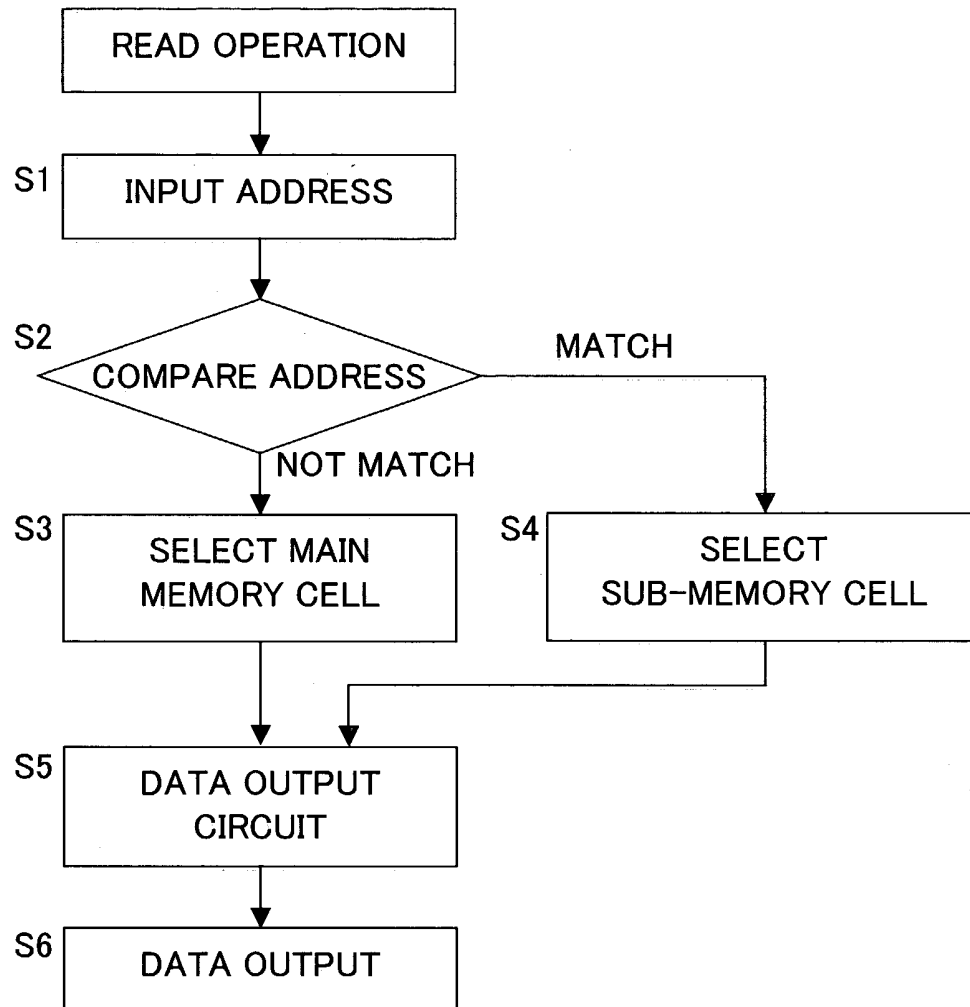


FIG. 8

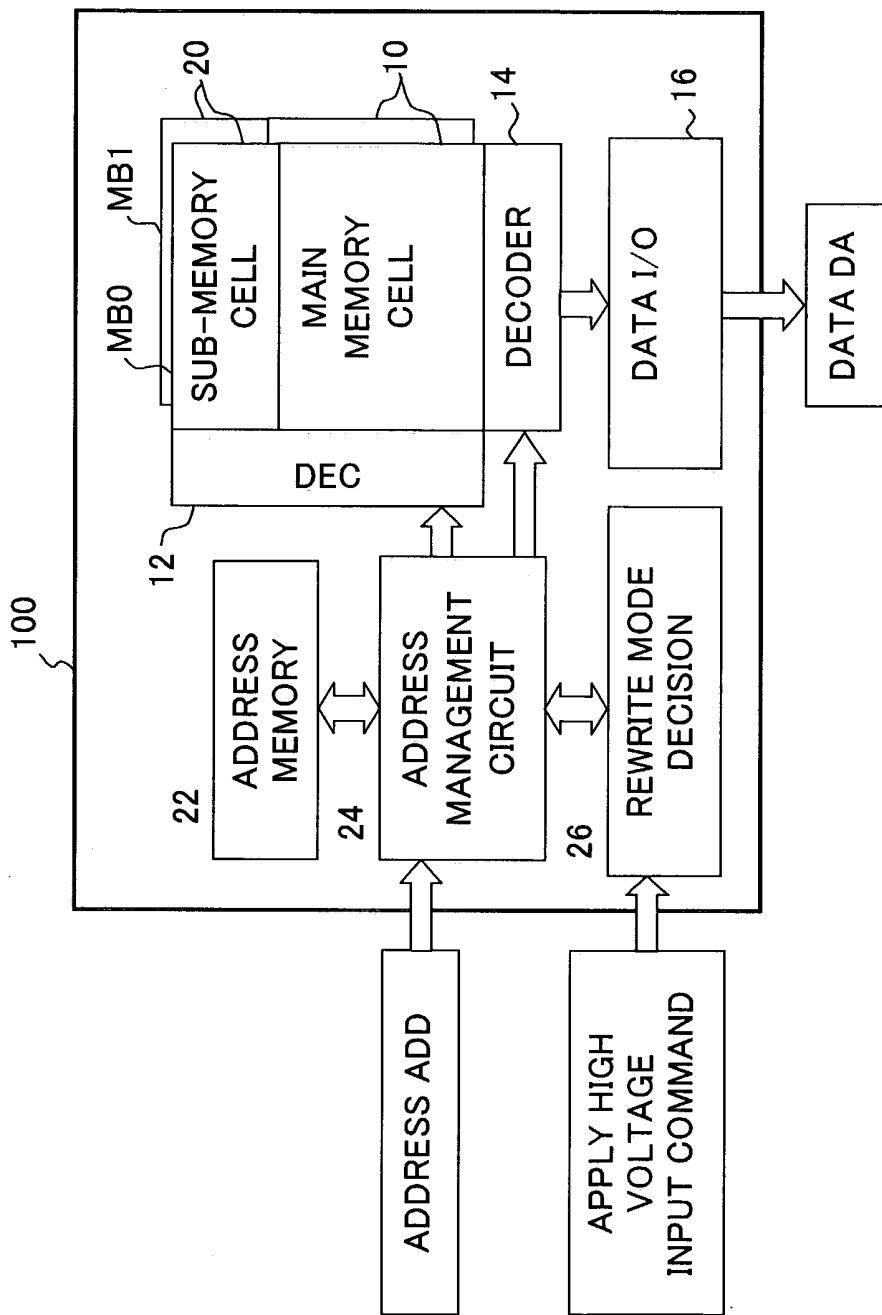


FIG. 9

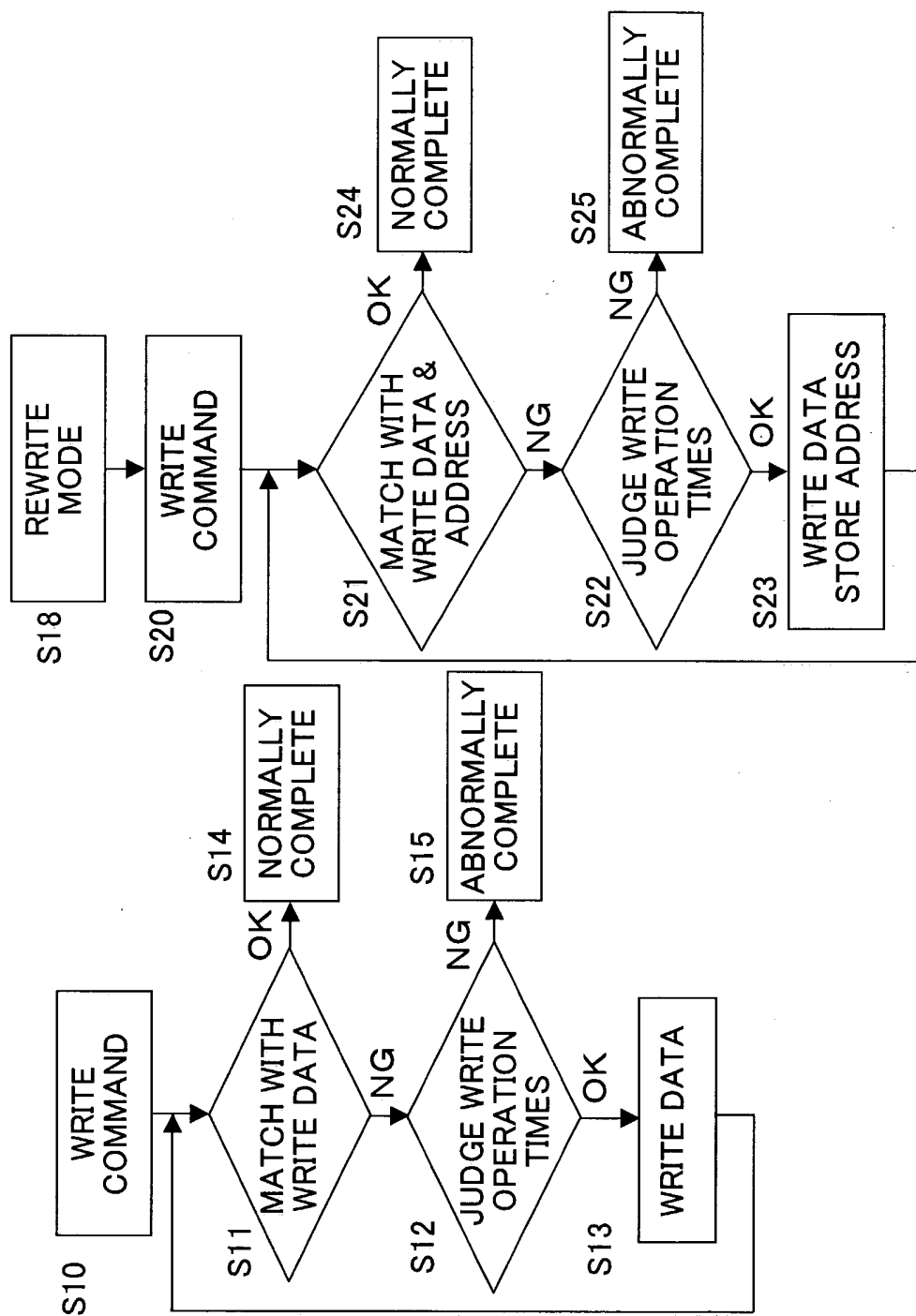


FIG. 10

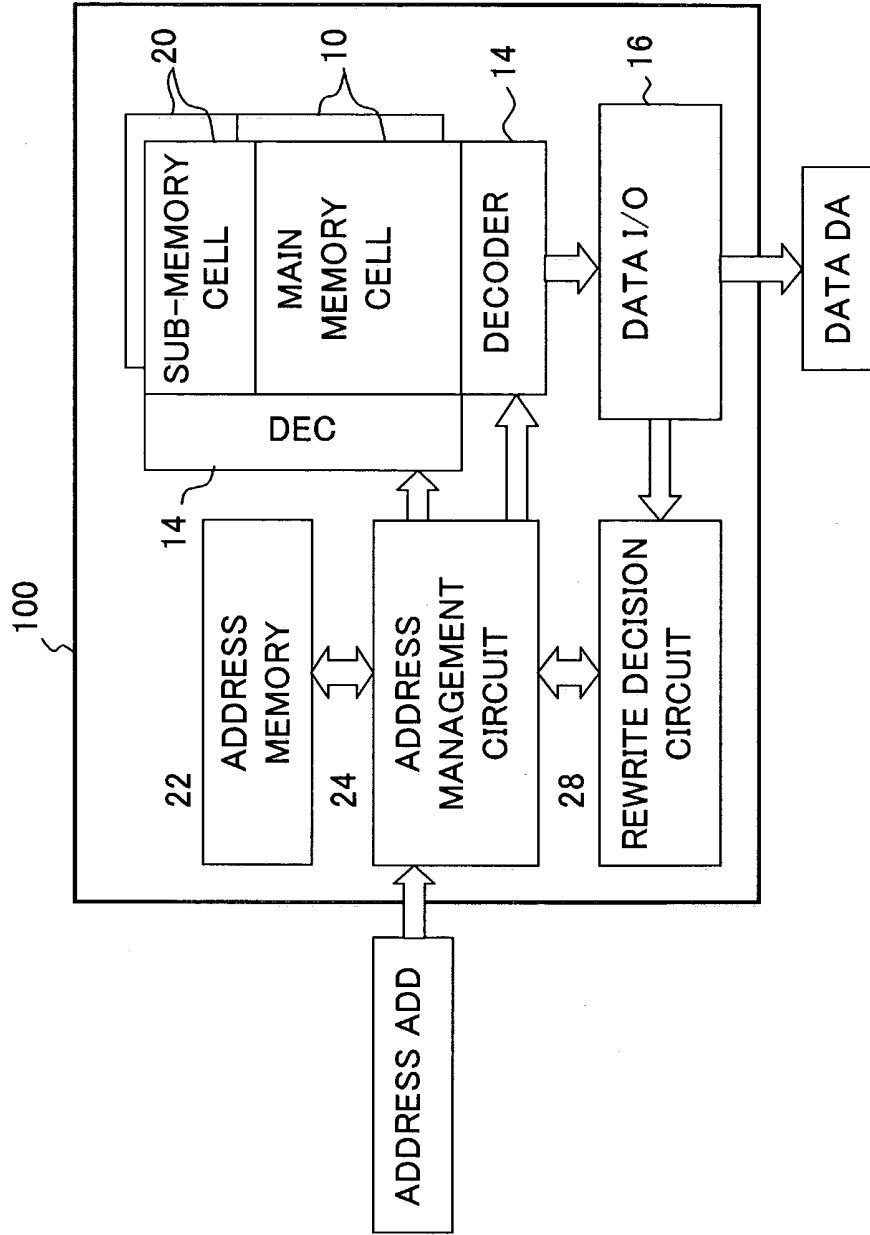


FIG. 11

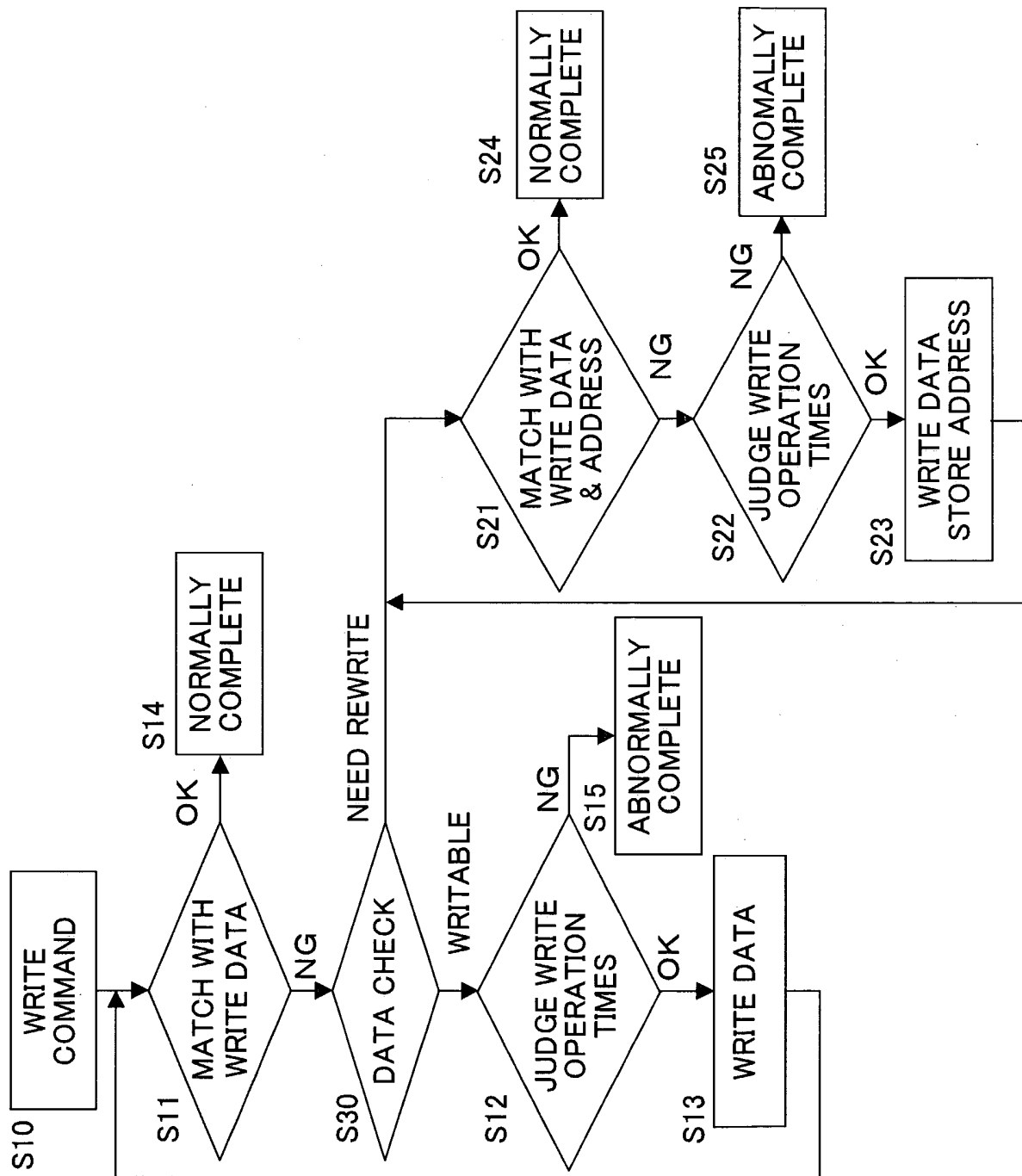


FIG. 12

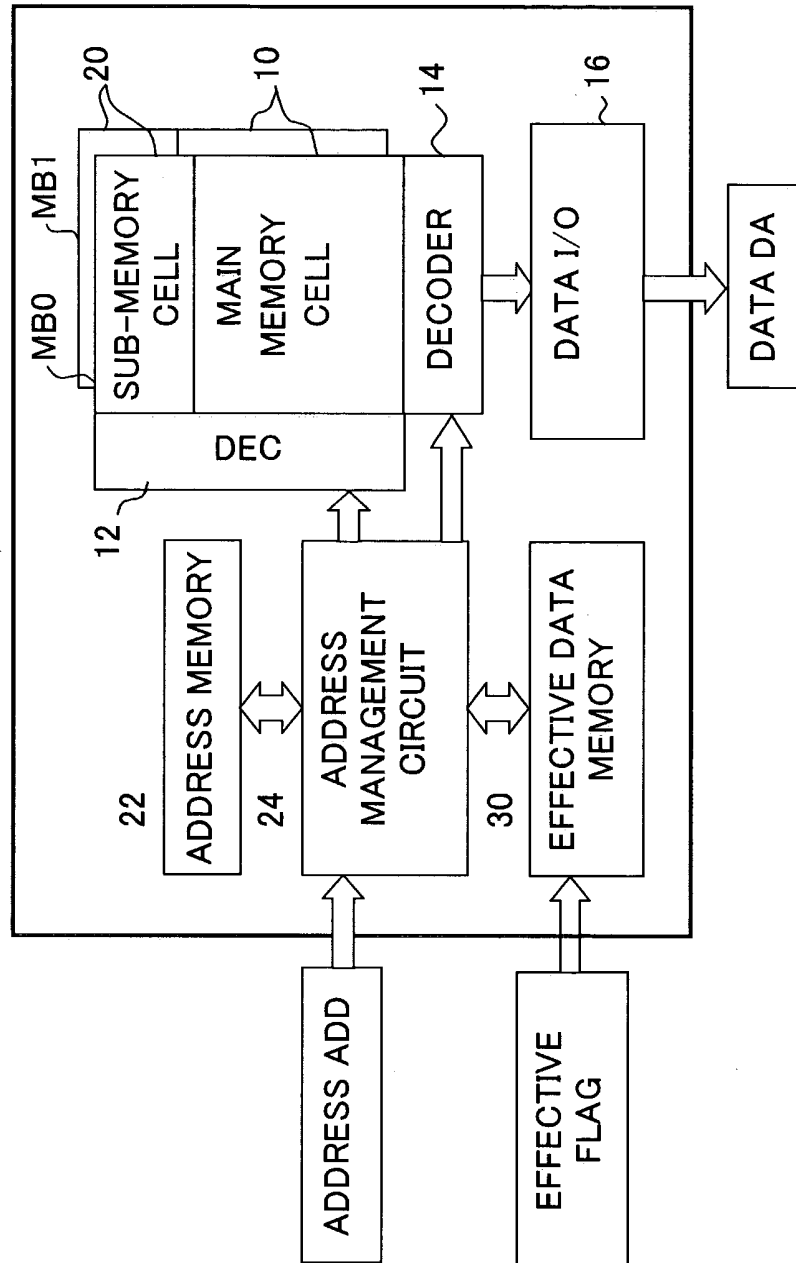


FIG. 13

